

Parameters Subject to Change Without Notice

### FEATURES

- DiSEqC 1.x Compatibility
- Up to 550mA Continuous Output Current
- 8V to 14V Input Voltage
- Tracking Switch-Mode Power Converter for Lowest Power Dissipation, High Efficiency
- Boost Converter with Internal Switch
- Low Noise Linear Regulator Output
- 1V Line Drop Compensation
- External Input 22KHz Tone
- Output Short Circuit Robust
- Input Voltage UVLO Protection
- Over Temperature Protection

### APPLICATIONS

- LNB Power Supply and Control for Satellite Set-Top Boxes

### DESCRIPTION

JW4002 is a monolithic linear and switching voltage regulator, specially designed to provide efficient, low noise power to Satellite receiver's RF LNB (Low Noise Block) converter via coaxial cable.

The JW4002 integrates a current mode boost converter followed by a tracking linear regulator. A high switching frequency is chosen to minimize the size of passive filtering components, further assisting in cost reduction. The tracking linear regulator protects the output against overload or short circuit.

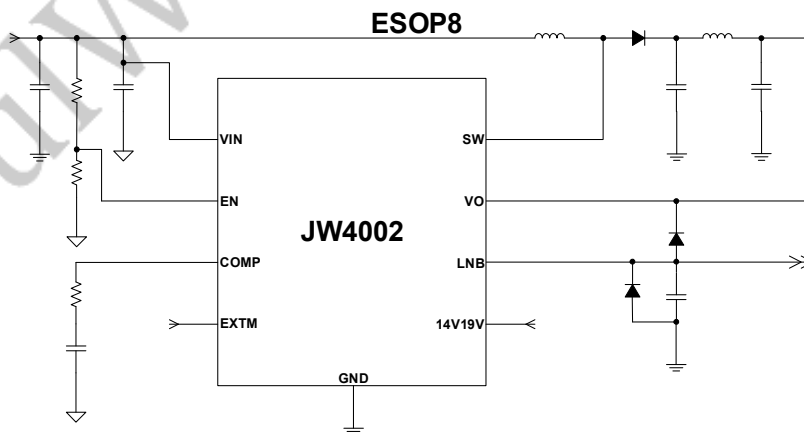
The JW4002 provides a number of features described in DiSEqC. For DiSEqC communications, the 22 KHz tone signal can be generated from external clock, all the way down to no-load.

JW4002 is available in ESOP8 packages.

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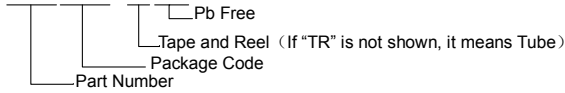
### TYPICAL APPLICATION



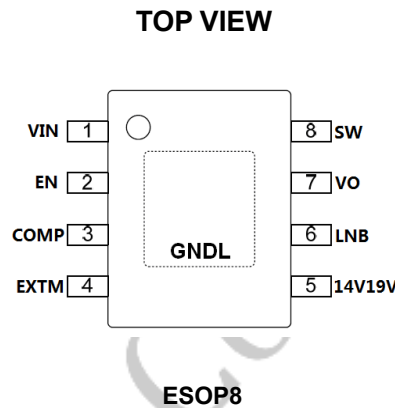
**ORDERING INFORMATION**

LEAD FREE FINISH	TAPE AND REEL	PACKAGE	TOP MARKING	JUNCTION TEMPERATURE RANGE
JW4002ESOP#PBF	JW4002ESOP#TRPBF	eSOP8	JW4002	- 40 °C to 125 °C

JWXXXXPPPP#TRPBF



**PIN CONFIGURATION**



**ABSOLUTE MAXIMUM RATING<sup>1)</sup>**

VIN,EN,LNB,VO,SW	-0.3V to 24V
All Other Pins	-0.3V to 7V
Junction Temperature	150°C
Lead Temperature	260°C
Storage Temperature	-65°C to +150°C

**RECOMMEND OPERATING RANGE**

Supply Voltage V <sub>IN</sub>	8V to 14V
Output Voltage V <sub>LNB</sub>	13V/14V/18V/19V
Ambient Temperature	-25°C to +85°C
Maximum Junction Temperature (T <sub>J</sub> )	125°C

**THERMAL RESISTANCE<sup>4)</sup>**      $\theta_{JA}$     $\theta_{Jc}$

ESOP8	50	10 °C /W
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**Note :**

- 1) Exceeding these ratings may damage the device.
- 2) The JW4002 guarantees robust performance from -40°C to 150°C junction temperature. The junction temperature range specification is assured by design, characterization and correlation with statistical process controls.
- 3) The JW4002 includes thermal protection that is intended to protect the device in overload conditions. Thermal protection is active when junction temperature exceeds the maximum operating junction temperature. Continuous operation over the specified absolute maximum operating junction temperature may damage the device.
- 4) Measured on JESD51-7, 4-layer PCB.

**ELECTRICAL CHARACTERISTICS**

*V<sub>IN</sub>=12V, T<sub>A</sub>=25 °C, unless otherwise stated.*

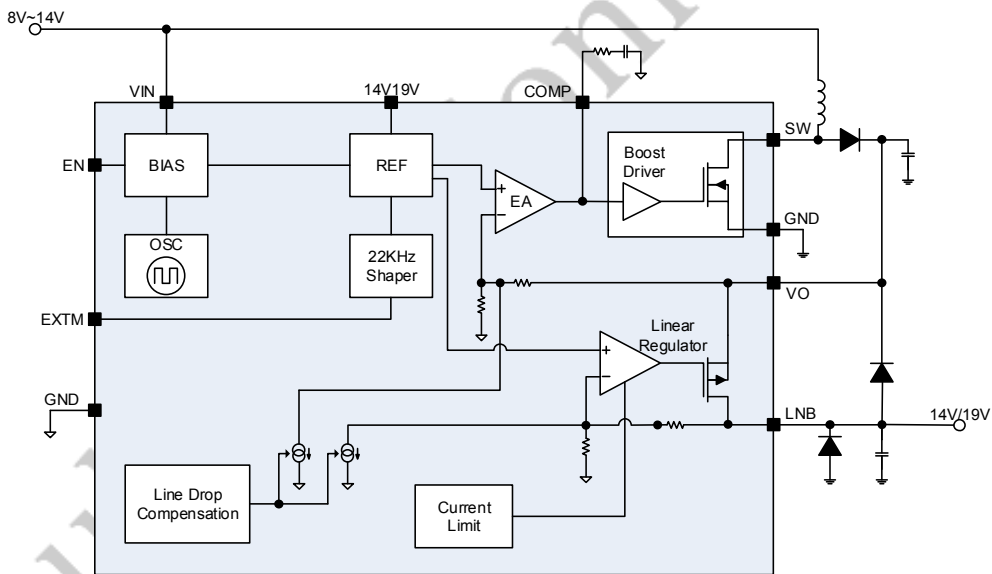
Item	Symbol	Condition	Min.	Typ.	Max.	Units
<b>General</b>						
Input Voltage Range	V <sub>IN</sub>		8	12	14	V
Input Supply Current	I <sub>IN</sub>	EN=High, no load		2		mA
Under Voltage Lockout	V <sub>UVLO</sub>	V <sub>IN</sub> rising	7.4	7.7	8	V
UVLO Hysteresis	V <sub>UVLO_HYS</sub>	V <sub>IN</sub> falling		350		mV
EN Input Logic Threshold	V <sub>EN_HIGH</sub>	V <sub>EN</sub> rising	2			V
	V <sub>EN_LOW</sub>	V <sub>EN</sub> falling			0.8	V
14V19V Input Logic Threshold	V <sub>14V19V_HIGH</sub>	V <sub>14V19V</sub> rising	2			V
	V <sub>14V19V_LOW</sub>	V <sub>14V19V</sub> falling			0.8	V
COMP Pin Current	I <sub>COMP_DISCHG</sub>	COMP sinking	19.8	22	24.2	μA
	I <sub>COMP_CHG</sub>	COMP sourcing	19.8	22	24.2	μA
<b>Boost Converter</b>						
Boost Switch ON Resistance	R <sub>DSON</sub>	I <sub>SW</sub> =1A		200		mΩ
Boost Switching Frequency	F <sub>SW</sub>		634	704	774	KHz
Boost Switch Peak Current Limit	I <sub>SW_LIMIT</sub>		2.8	3.1	3.4	A
Minimum On Time	T <sub>ON_MIN</sub>			100		nS
Maximum On Time	T <sub>ON_MAX</sub>			1.3		μS
<b>Linear Regulator</b>						
Output Voltage	V <sub>LNB</sub>	14V19V=Low, I <sub>LNB</sub> =0mA-100mA	13.9	14.2	14.5	V
		14V19V=High, I <sub>LNB</sub> =0mA-100mA	18.9	19.3	19.7	V
Output Line Regulation		8V≤V <sub>IN</sub> ≤14V		4	40	mV
Output Load Regulation		10mA≤I <sub>LNB</sub> ≤500mA, 14V19V=Low		60		mV
		10mA≤I <sub>LNB</sub> ≤500mA, 14V19V=High		80		mV
Dropout Voltage	V <sub>DROP</sub>	V <sub>O</sub> -V <sub>LNB</sub> , I <sub>LNB</sub> =500mA, T <sub>MOD</sub> =High		1		V
		V <sub>O</sub> -V <sub>LNB</sub> , I <sub>LNB</sub> =500mA, T <sub>MOD</sub> =Low, EXT <sub>M</sub> =Low		0.6		V
<b>Tone Signal</b>						
Tone Signal Frequency	F <sub>TONE</sub>	T <sub>A</sub> =25 °C		22		KHz

Peak-to-Peak Amplitude		ILNB=0mA-100mA	0.4	0.65	0.9	V
Rising and Falling Time	T <sub>TRAN</sub>	ILNB=100mA, C <sub>LNB</sub> =0.1μF		7		μS
EXTM Input Logic Threshold	V <sub>EXTM_HIGH</sub>	V <sub>EXTM</sub> rising	2			V
	V <sub>EXTM_LOW</sub>	V <sub>EXTM</sub> falling			0.8	V
<b>Over Current Protection</b>						
Output Current Limit	I <sub>LNB_LIMIT</sub>		0.5	0.6	0.7	A
Dynamic Overload Protection On Time	T <sub>ON</sub>	Time to onset of shutdown		3		mS
Dynamic Overload Protection OFF Time	T <sub>OFF</sub>	Time to attempt to restart		2		S
<b>Over Temperature Protection</b>						
Over Temperature Shutdown Threshold	T <sub>SHDN</sub>			150		°C
OTP Hysteresis	T <sub>SHDN_HYS</sub>			10		°C

**PIN DESCRIPTION**

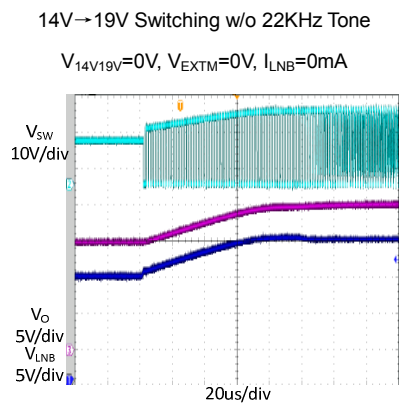
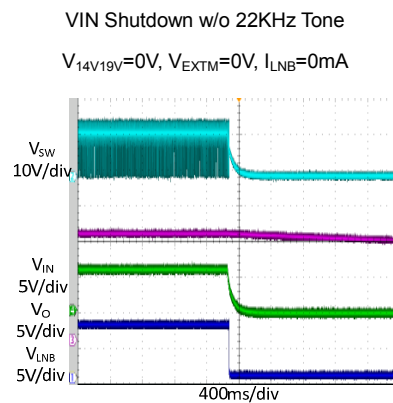
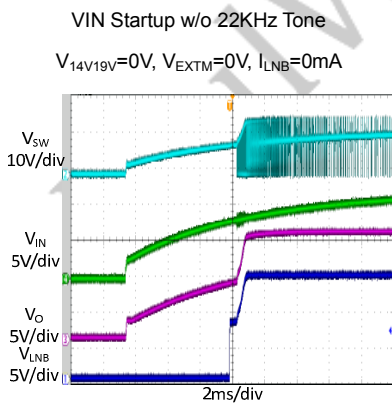
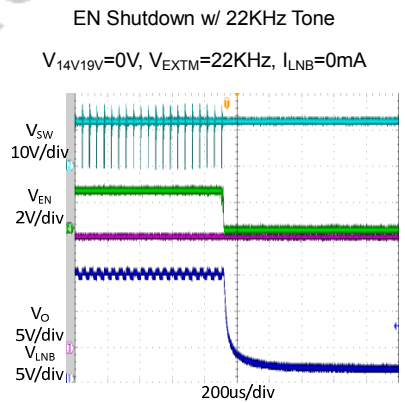
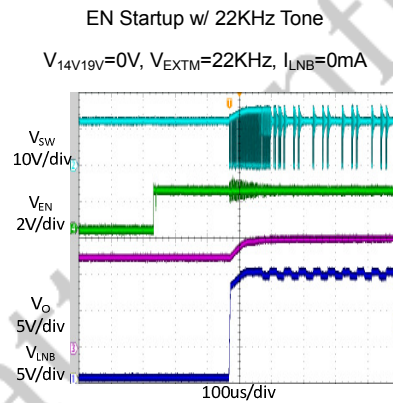
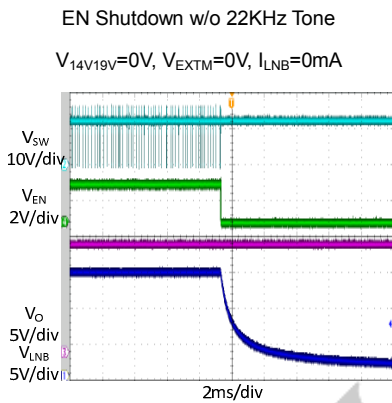
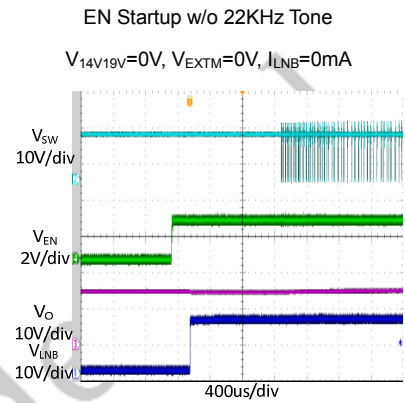
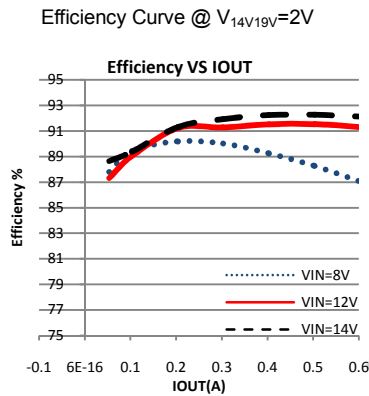
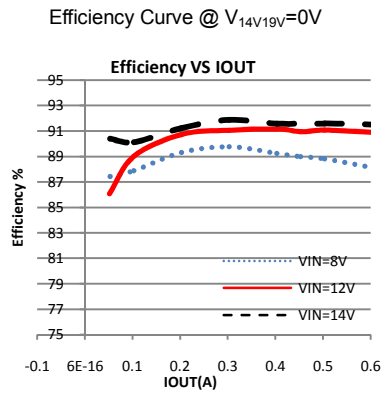
Pin No	Name	Description
1	VIN	Power Supply for the Chip
2	EN	Chip ON/OFF Control Input
3	COMP	Compensation for Boost Converter
4	EXTM	External Modulation 22KHz Tone signal Input.
5	14V19V	Select 14V or 19V for Output Voltage, Low for 14V and High for 19V
6	LNB	Output Voltage to LNB
7	VO	Tracking Supply Voltage to Linear Regulator
8	SW	Drain of the Internal Boost MOSFET
EP	GND	Ground

**BLOCK DIAGRAM**



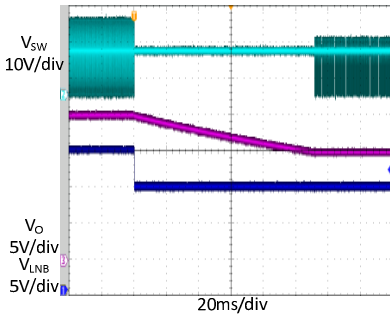
**TYPICAL PERFORMANCE CHARACTERISTICS**

Note: VIN=12V, VEN=2V, V14V19V=0V, VEXTM=0V, ILNB=0A, TA=+25°C, unless otherwise stated.



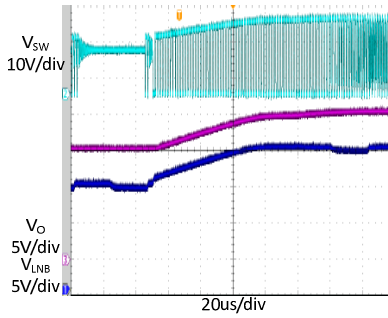
19V→14V Switching w/o 22KHz Tone

$V_{14V19V}=0V, V_{EXTM}=0V, I_{LNB}=0mA$



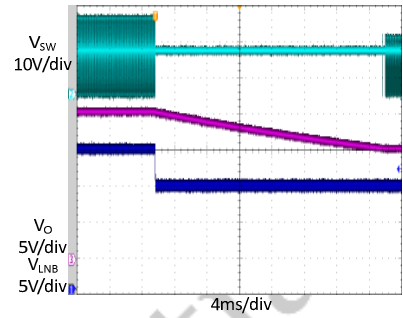
14V→19V Switching w/ 22KHz Tone

$V_{TMOD}=0V, V_{EXTM}=2V, I_{LNB}=0mA$



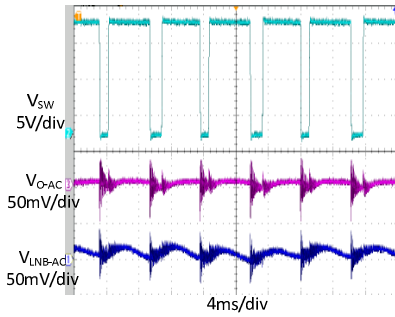
19V→14V Switching w/ 22KHz Tone

$V_{TMOD}=0V, V_{EXTM}=2V, I_{LNB}=0mA$



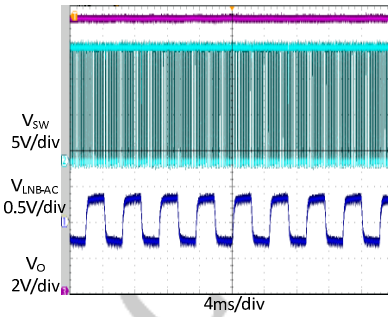
Output Voltage Ripple

$I_{LNB}=100mA$



22KHz Tone Signal

$V_{TMOD}=0V, V_{EXTM}=2V, I_{LNB}=120mA$



## **FUNCTIONAL DESCRIPTION**

The JW4002 is a single output voltage regulator for providing both supply voltage and control signal from satellite set-top box modules to LNB of the antenna port.

JW4002 has an integrated boost converter which generates the voltage to enable the linear post-regulator to work at a minimum dissipated power from a single supply source between 8V and 14V.

### **Boost Converter and Linear Regulator**

JW4002 integrates a fixed frequency, asynchronous boost converter with peak current control. The switching frequency is 704KHz typically.

The boost converter will automatically switch from PWM to PFM mode to reduce power dissipation at light load.

The output voltage of boost converter tracks the requested output voltage of linear regulator to allow the linear regulator to work with minimum drop-out voltage, typically 0.6V with 22KHz tone signal or 1V without 22KHz tone signal. The drop-out voltage will be automatically selected by detecting whether 22 KHz tone signal is activated or not internally.

### **14V/19V Switching & Soft-Start**

The output voltage of linear regulator can be set to 14V or 19V to select different polarization directions of LNB. A logic level low on 14V19V pin will set LNB to 14V while a high level set it to 19V.

### **Current Limit**

The maximum output current of linear regulator is 0.6A. When overload condition is detected, the output current will be regulated at ILIMIT level for 3mS. After this time period, if overload condition still exists, both boost converter and linear regulator will be shut down for 2S before they're resumed.

The boost converter integrates a cycle-by-cycle over current limit function which can guarantee JW4002 works with full load.

### **Tone Signal Generation**

In accordance with DiSEqC standards, a 22KHz tone signal can be generated from EXTM input externally and modulated onto LNB output to provide the LNB control information. If EXTM remains constant low or high which indicates no external 22 KHz tone signal input, the 22 KHz tone signal will be deactivated. In operation, when EXTM switches to constant low or high, the 22 KHz tone signal will be deactivated if EXTM remains constant logic low or high at the 7th falling edge of internal 22 KHz clock and reactivated upon 1st rising edge of signal on EXTM pin.

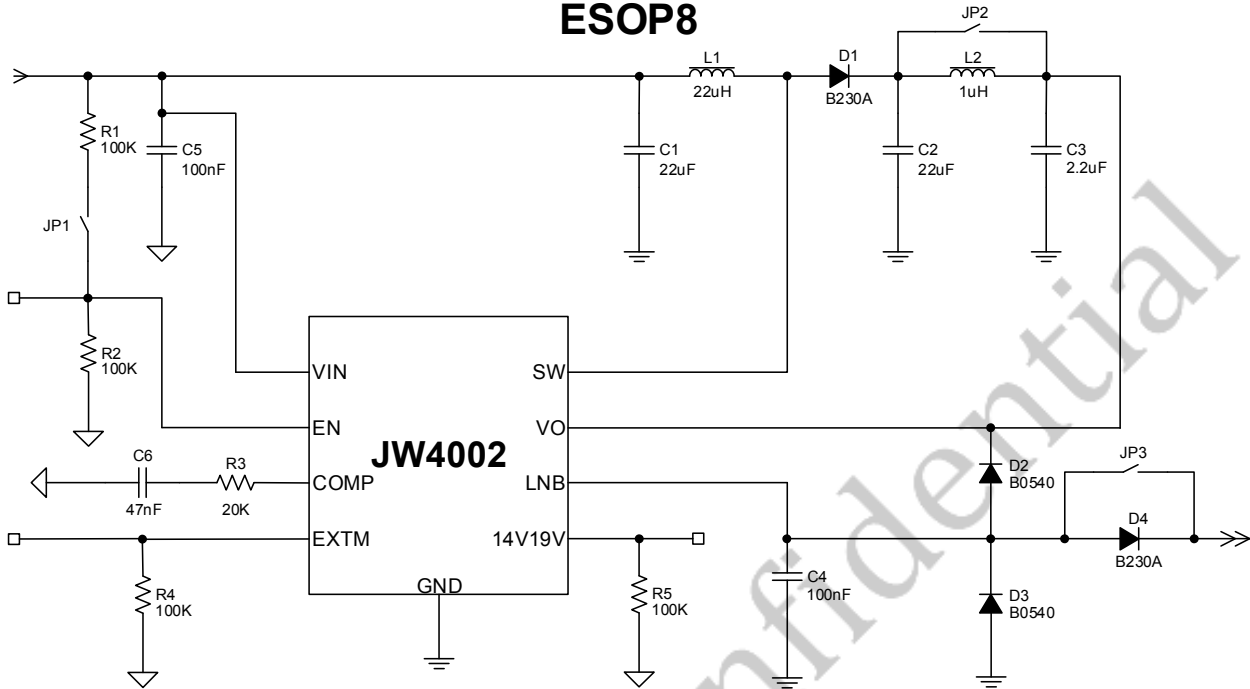
### **Thermal Protection**

When the junction temperature exceeds +150°C, the chip will be shut down. Once the junction temperature drops below 140°C typically, the chip will restart automatically.

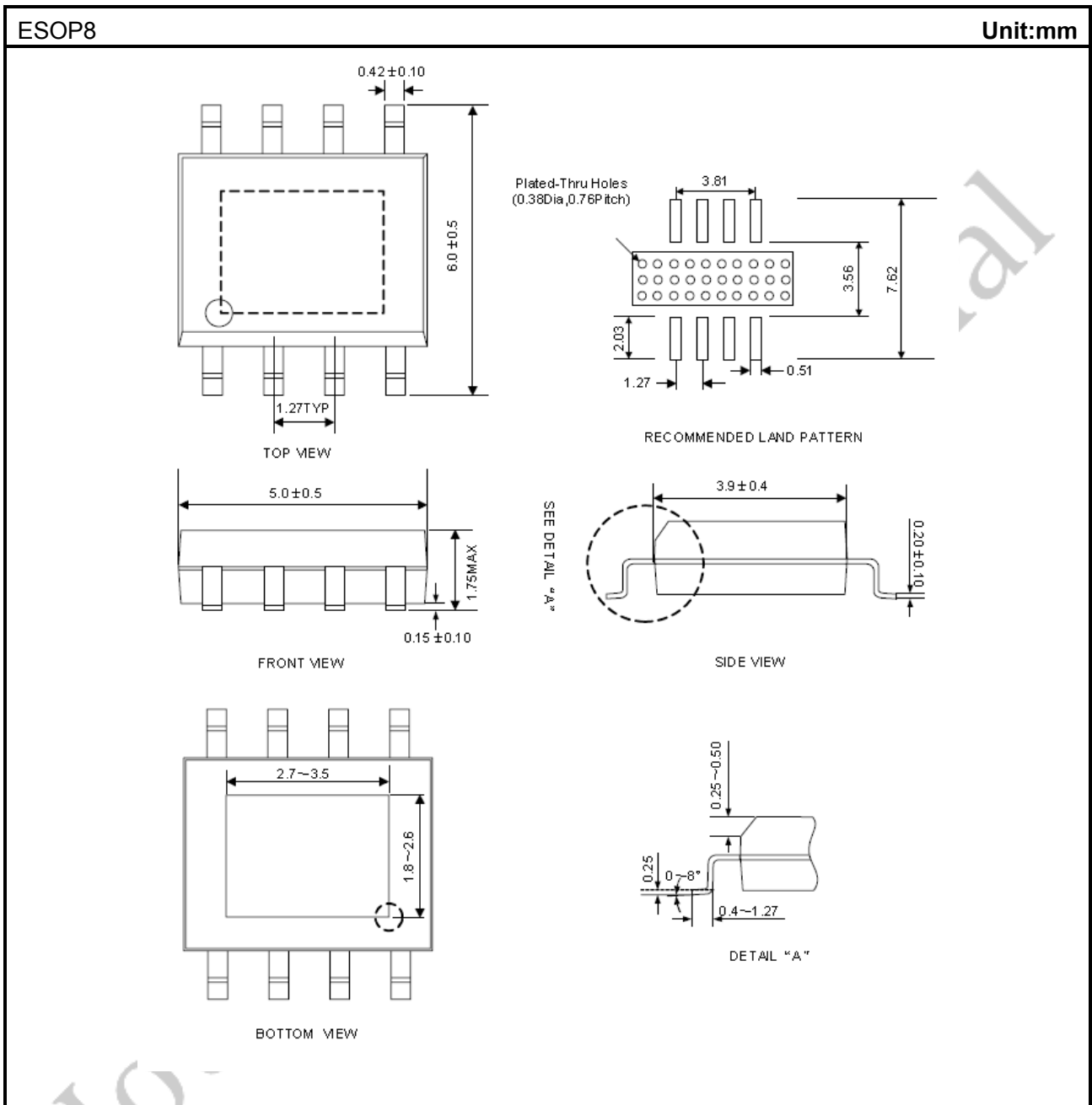


**APPLICATION REFERENCE**

**PCB Schematic  
ESOP8**



**PACKAGE OUTLINE**



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